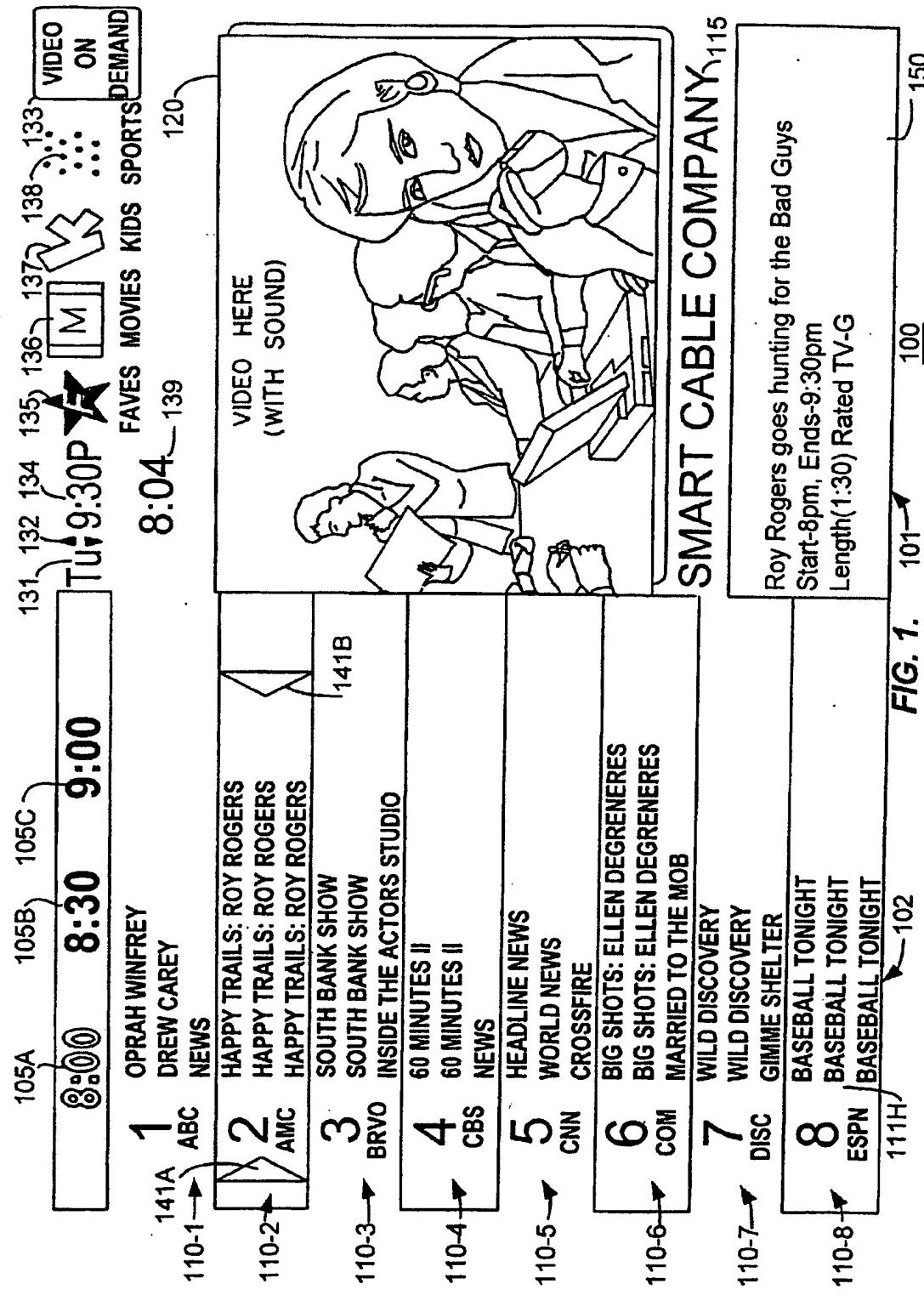


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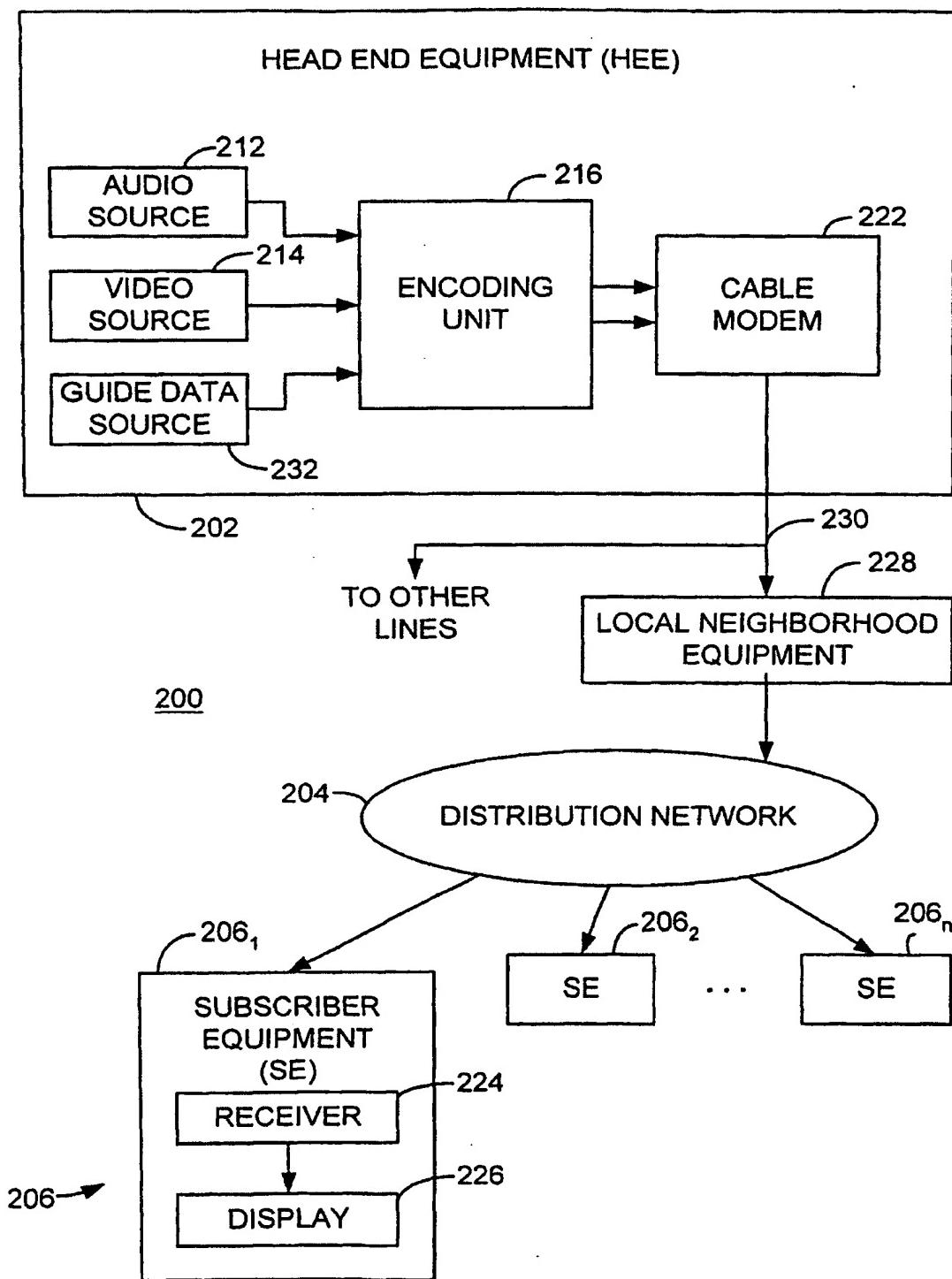


FIG. 2.

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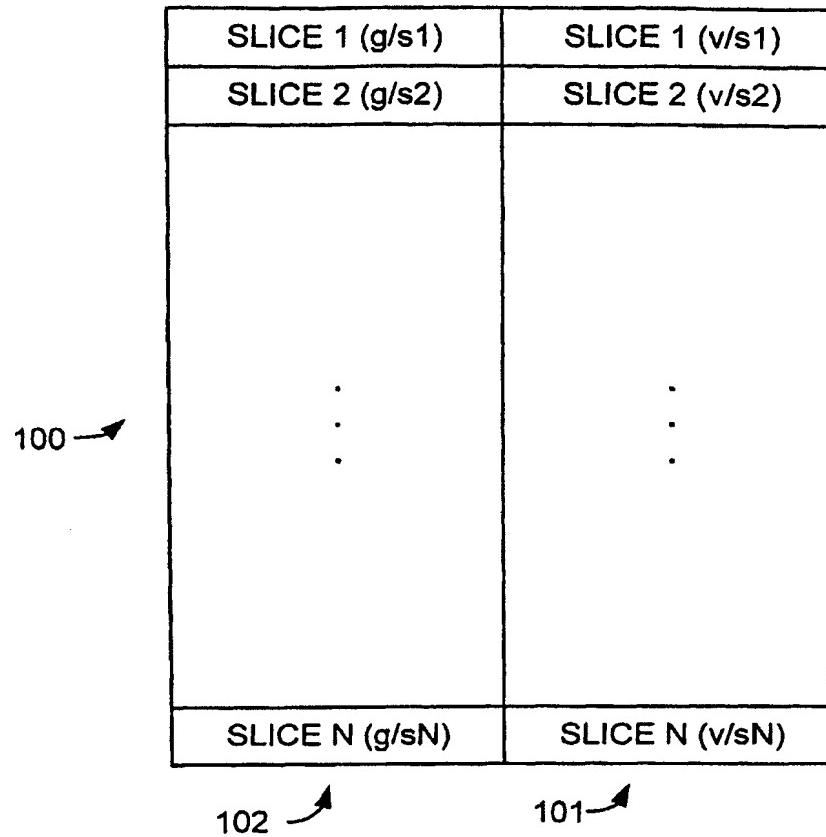


FIG. 3.



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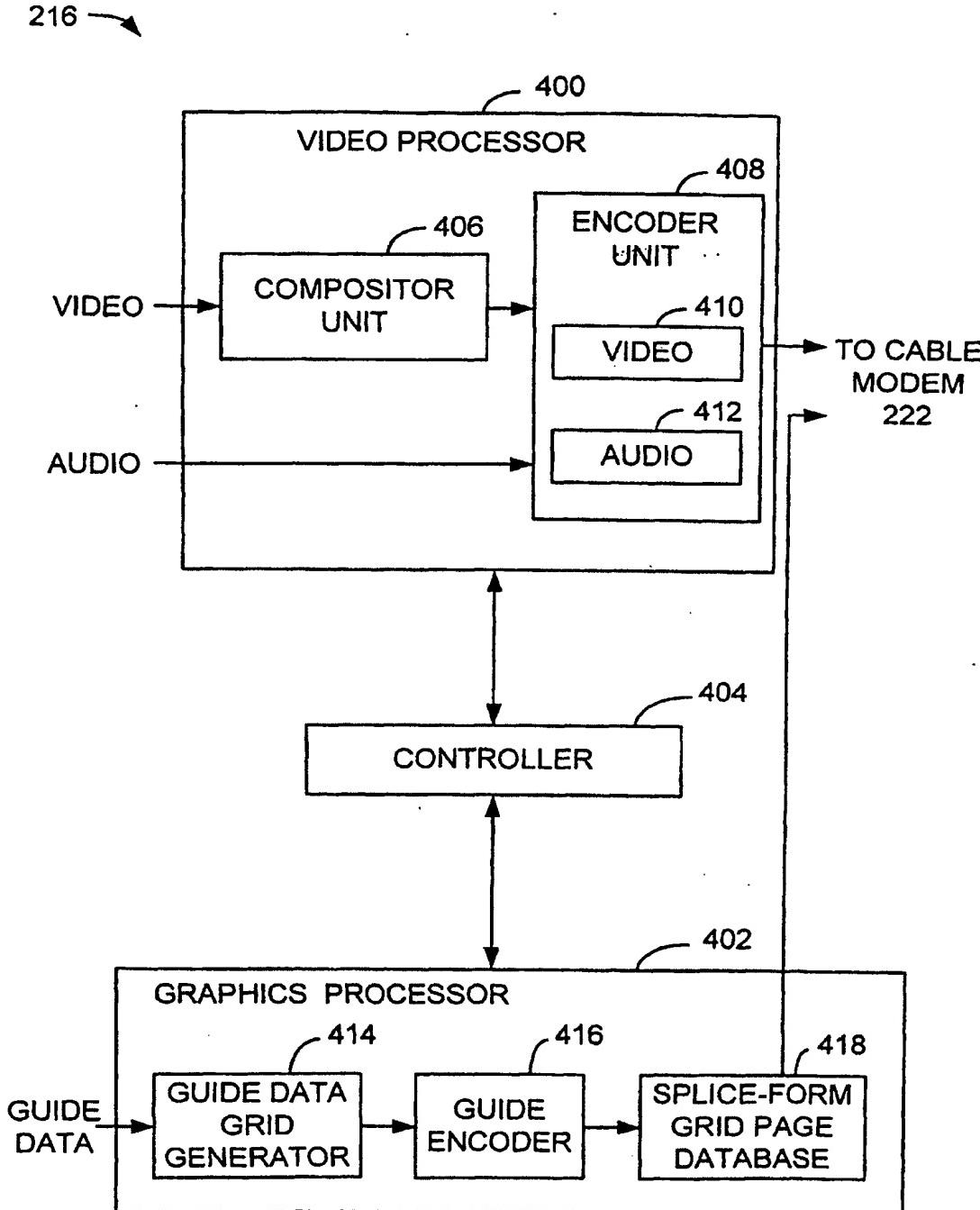


FIG. 4.



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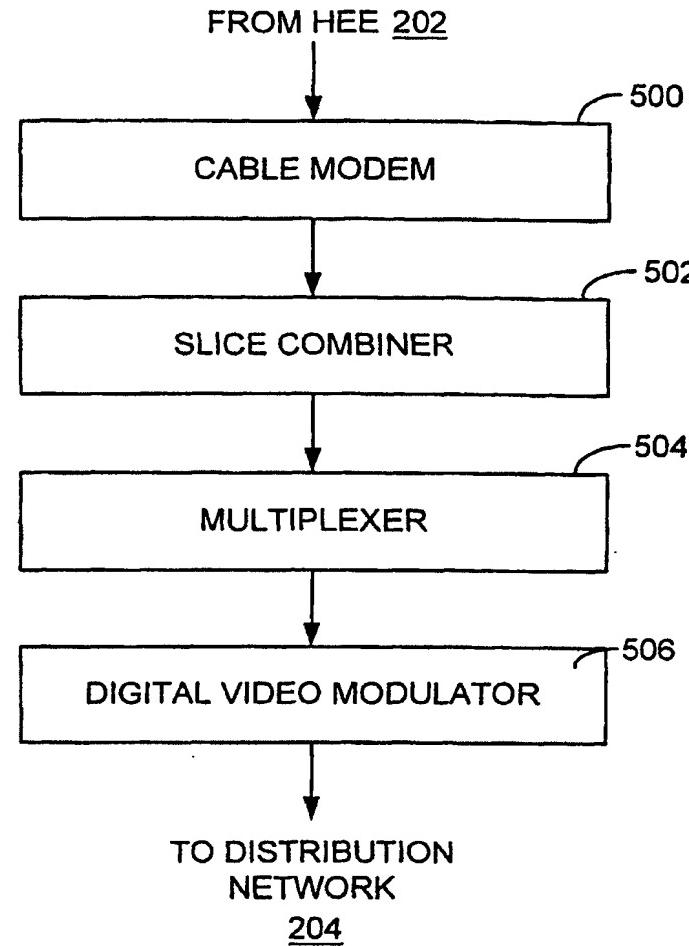


FIG. 5.



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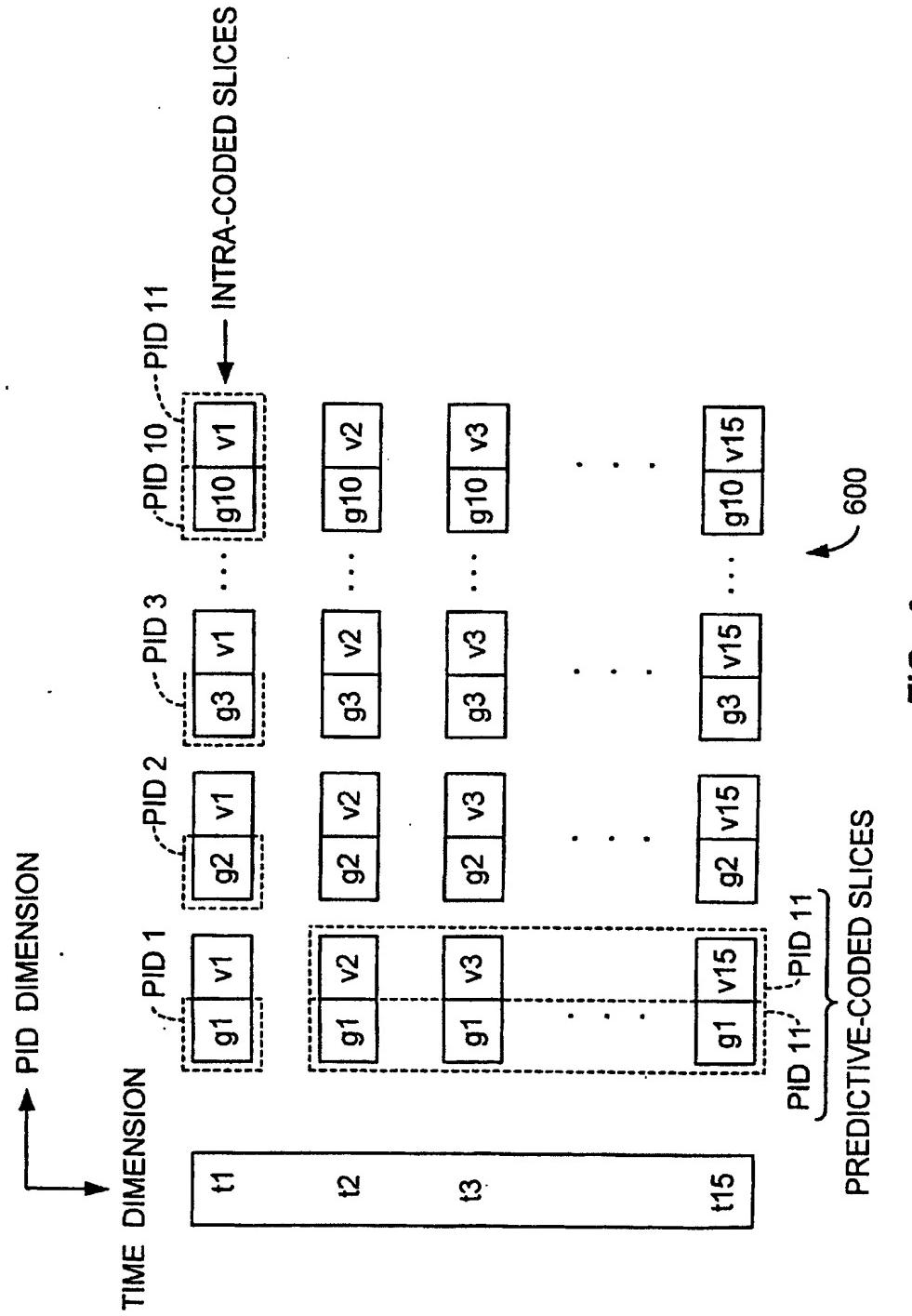


FIG. 6.



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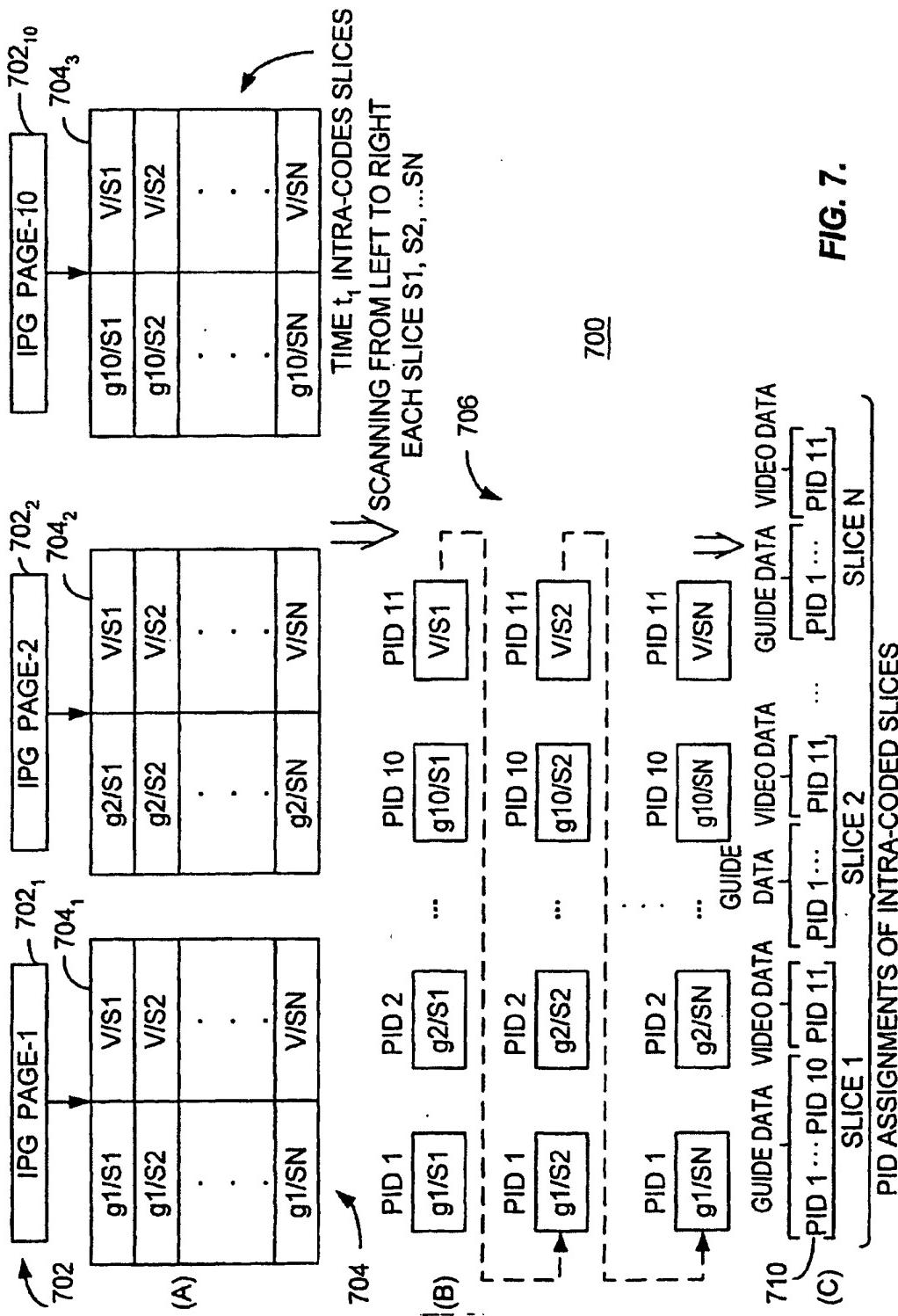


FIG. 7.



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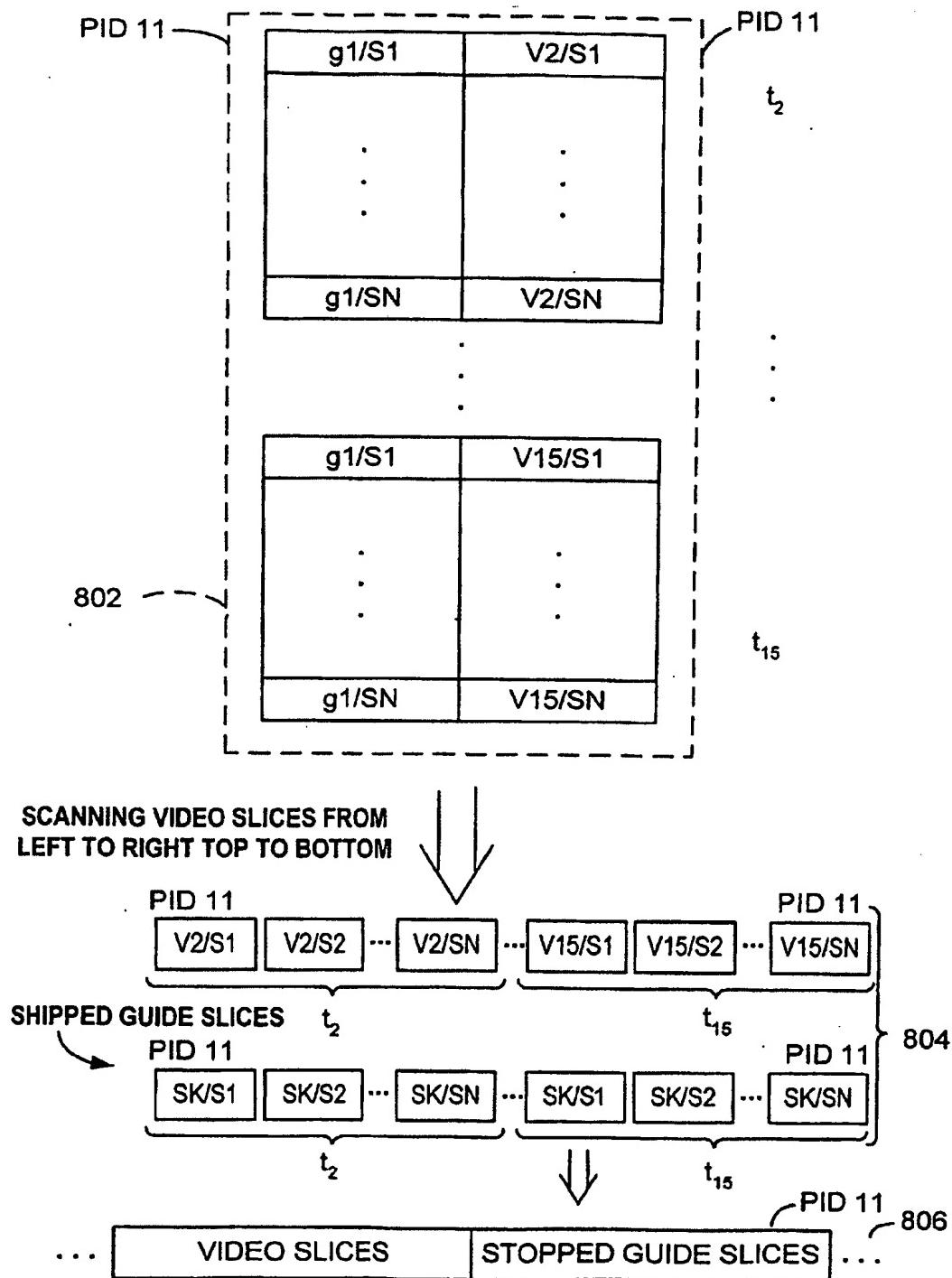


FIG. 8.

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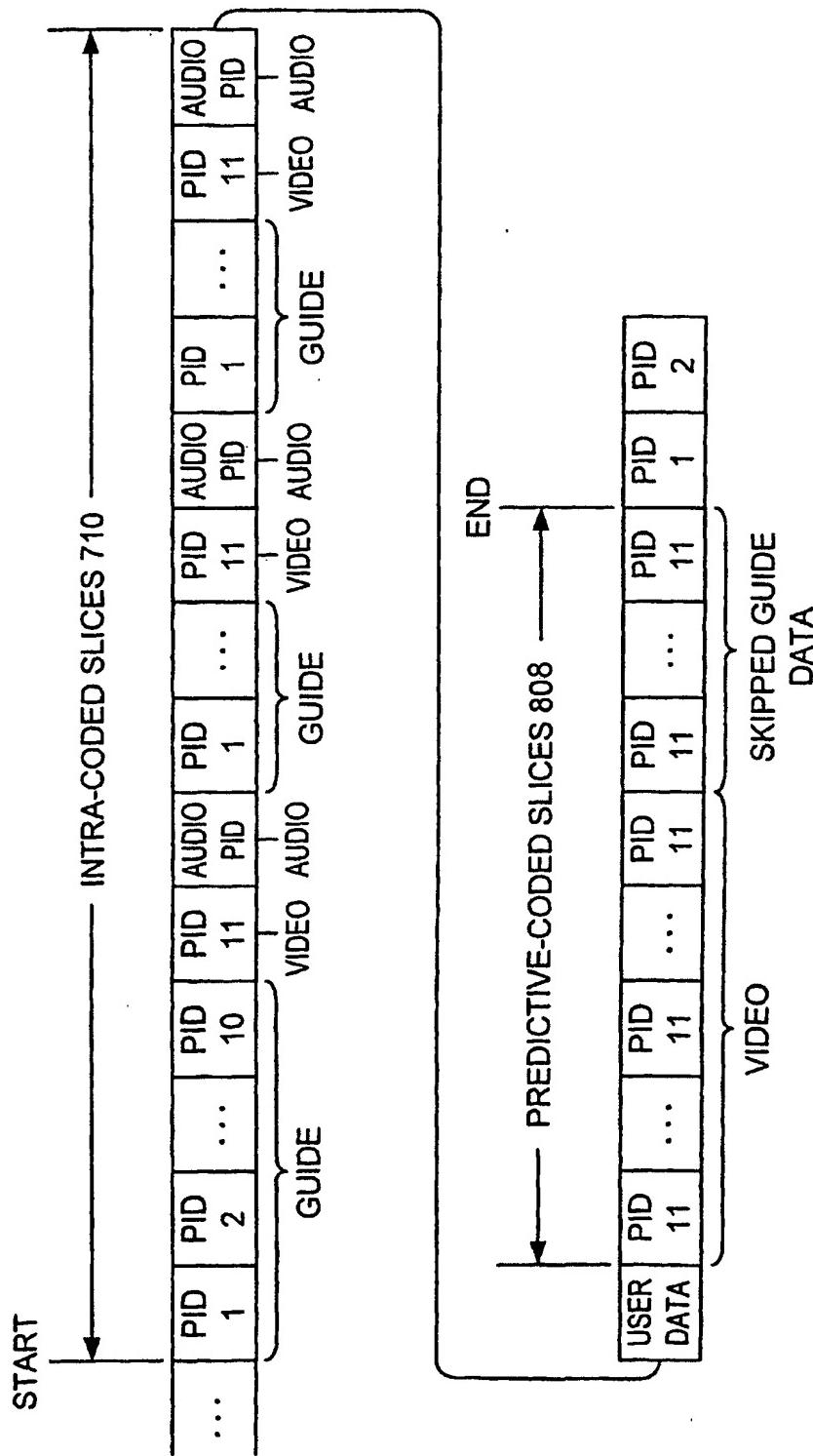


FIG. 9.



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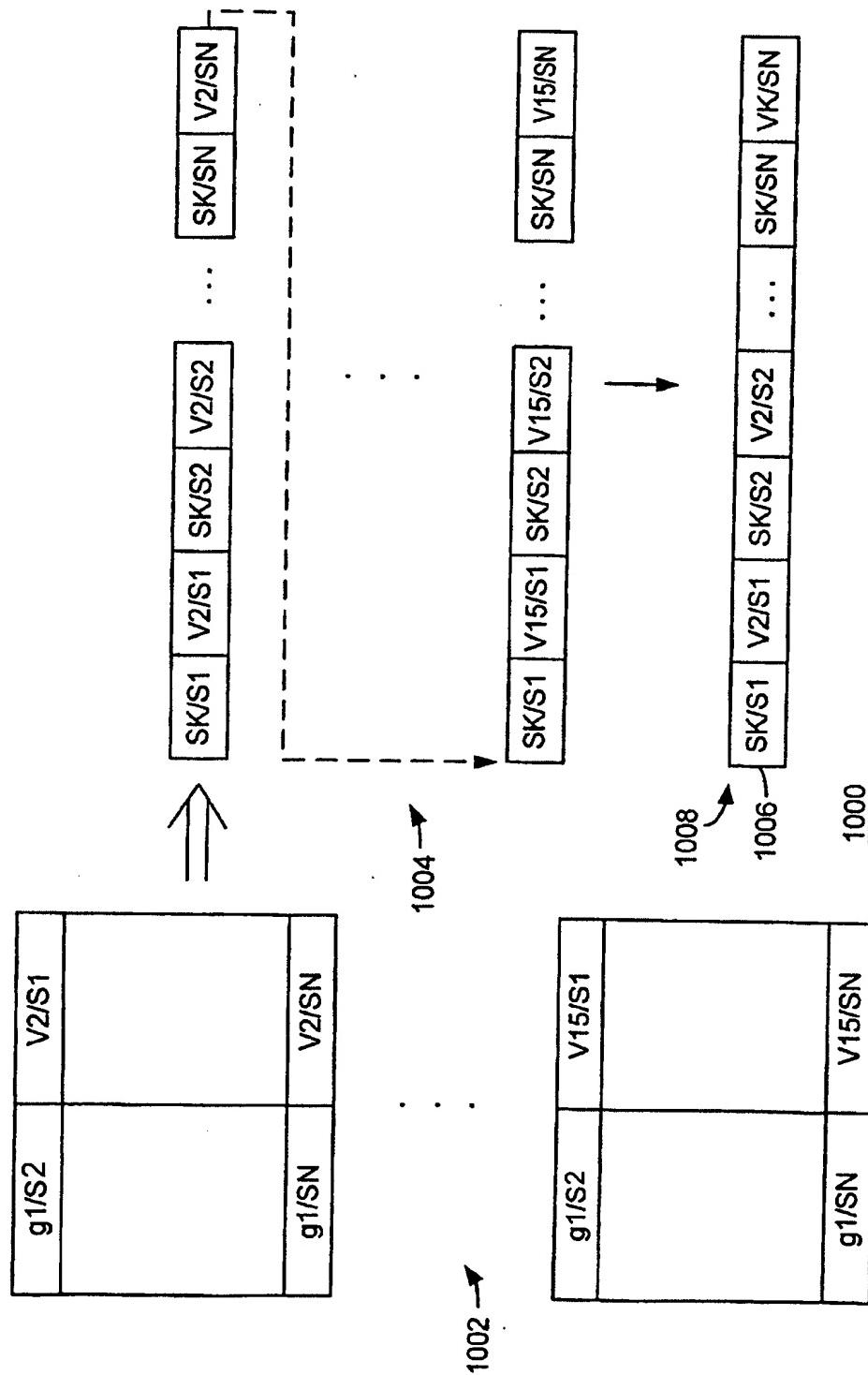


FIG. 10.



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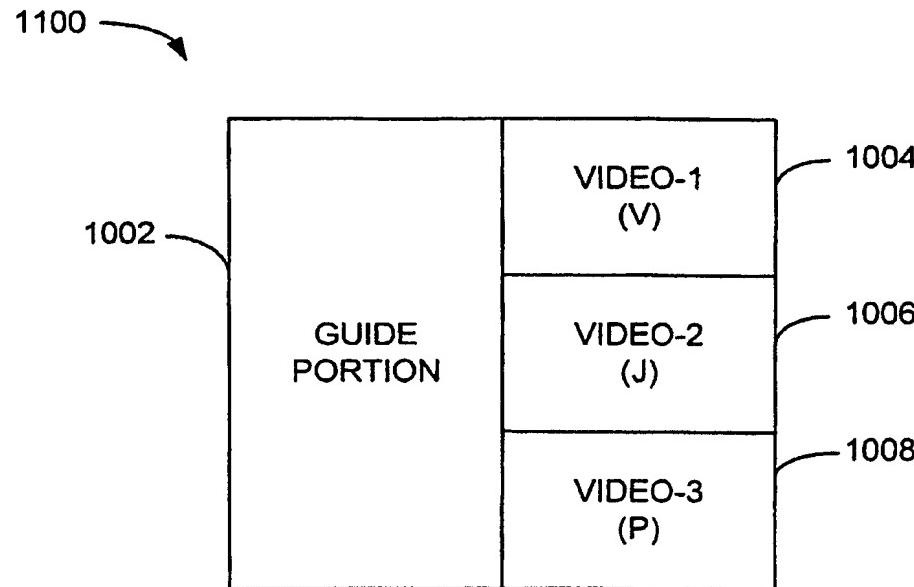


FIG. 11A.

GUIDE DATA SLICES

g/S1	V/S1
...	...
V/SM	...
J/SM+1	...
...	...
J/SL	...
P/SL+1	...
...	...
g/SN	P/SN

1st VIDEO SLICES

2nd VIDEO SLICES

3rd VIDEO SLICES

FIG. 11B.

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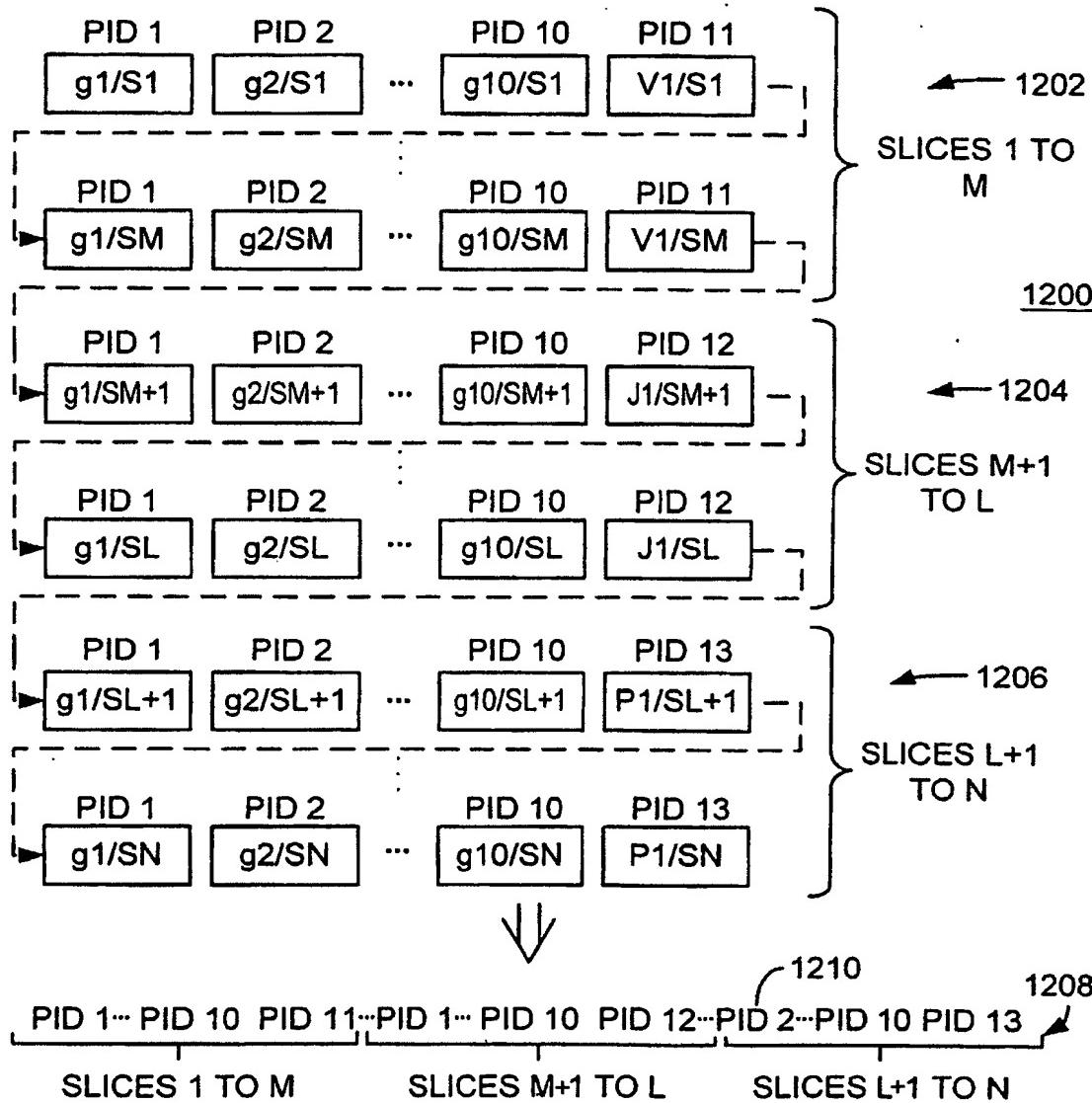


FIG. 12.



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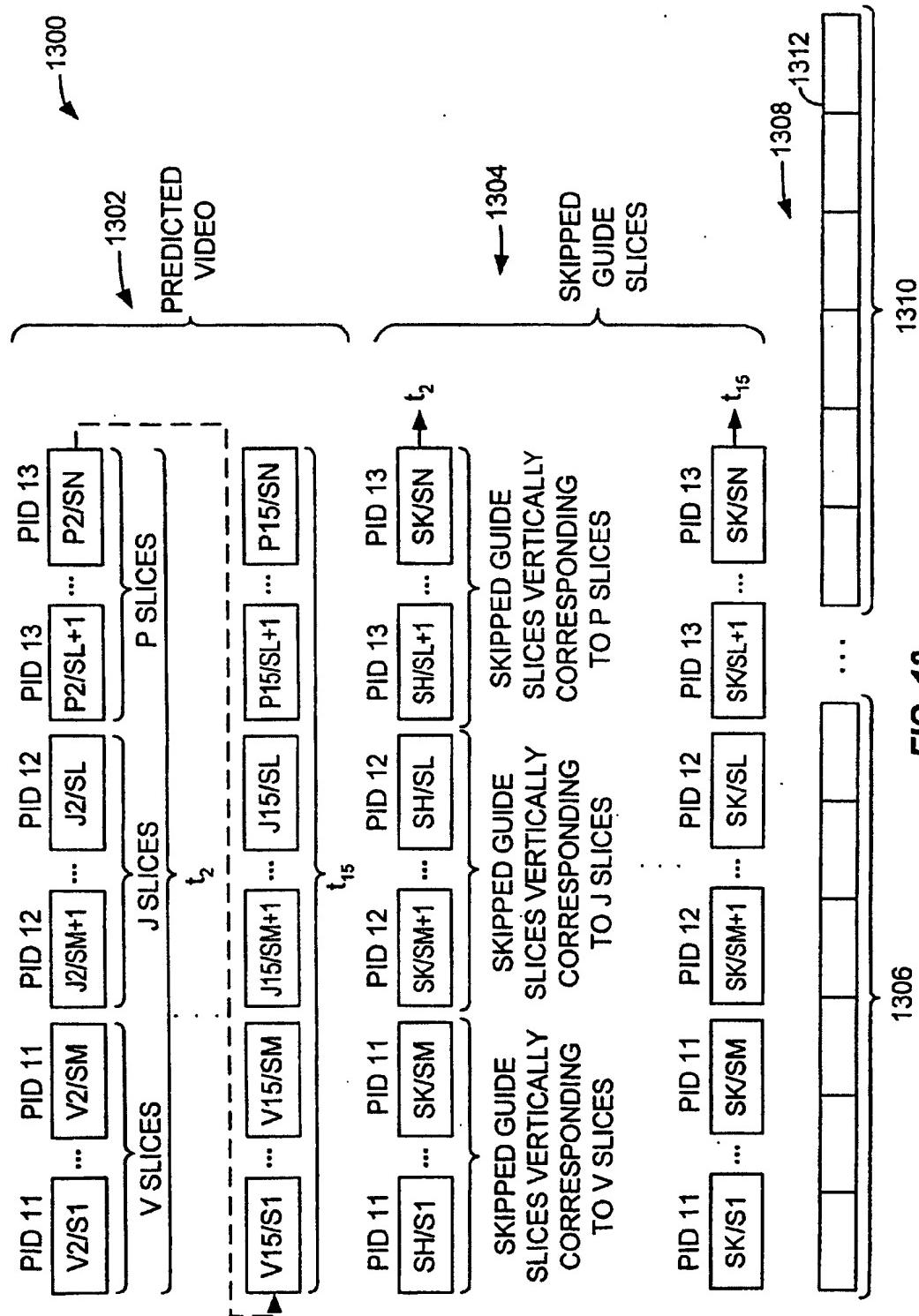
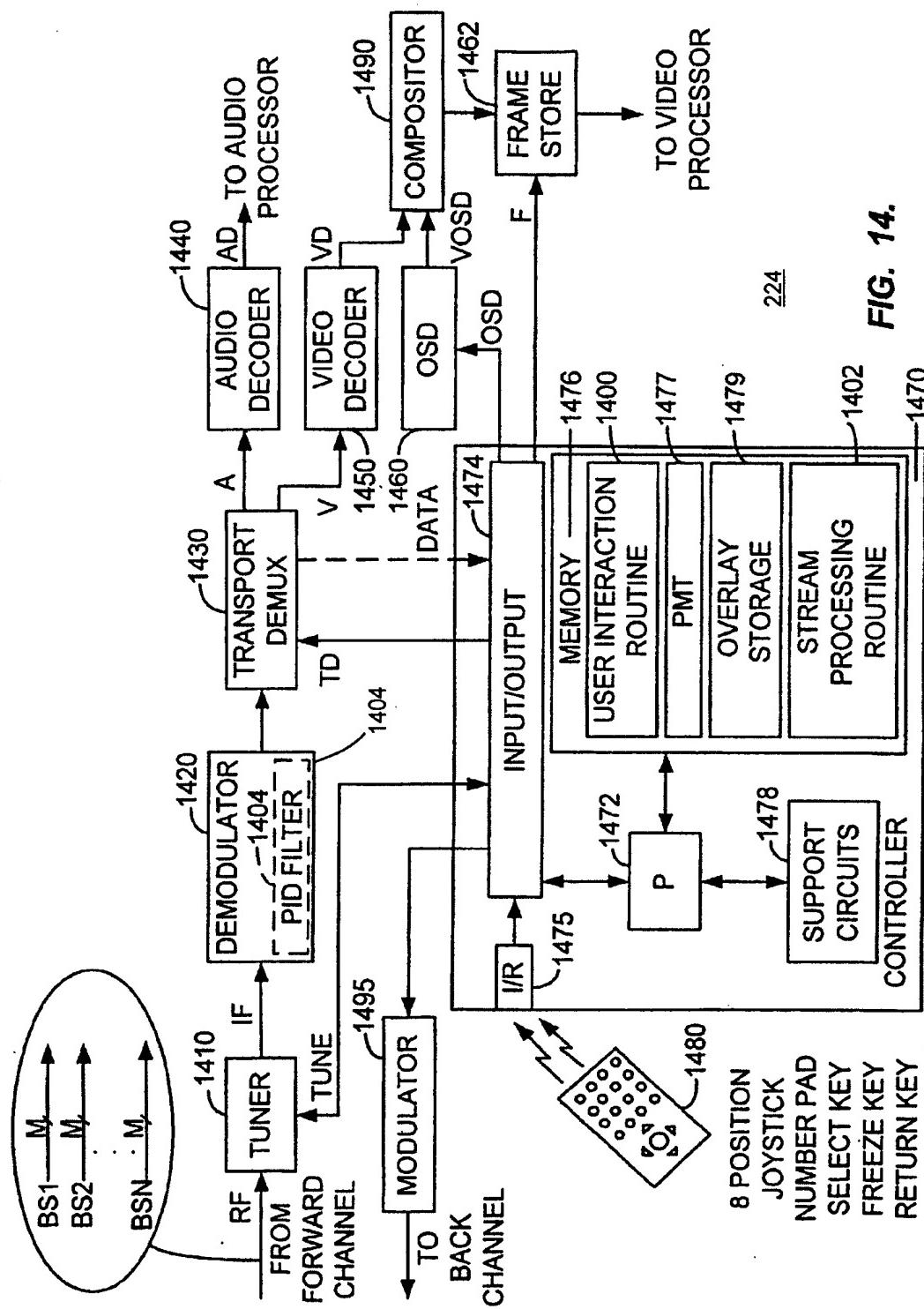


FIG. 13.



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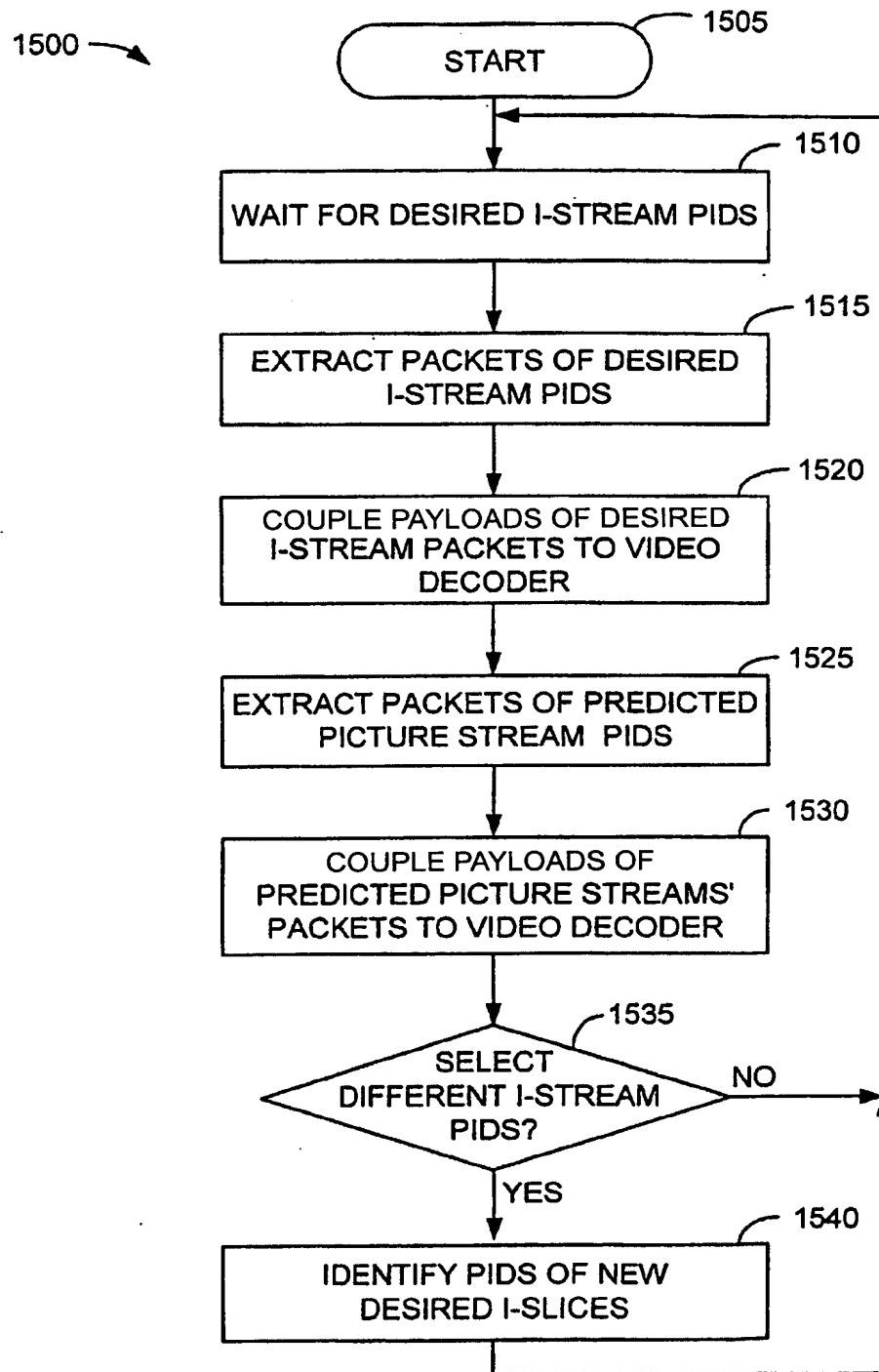


FIG. 15.



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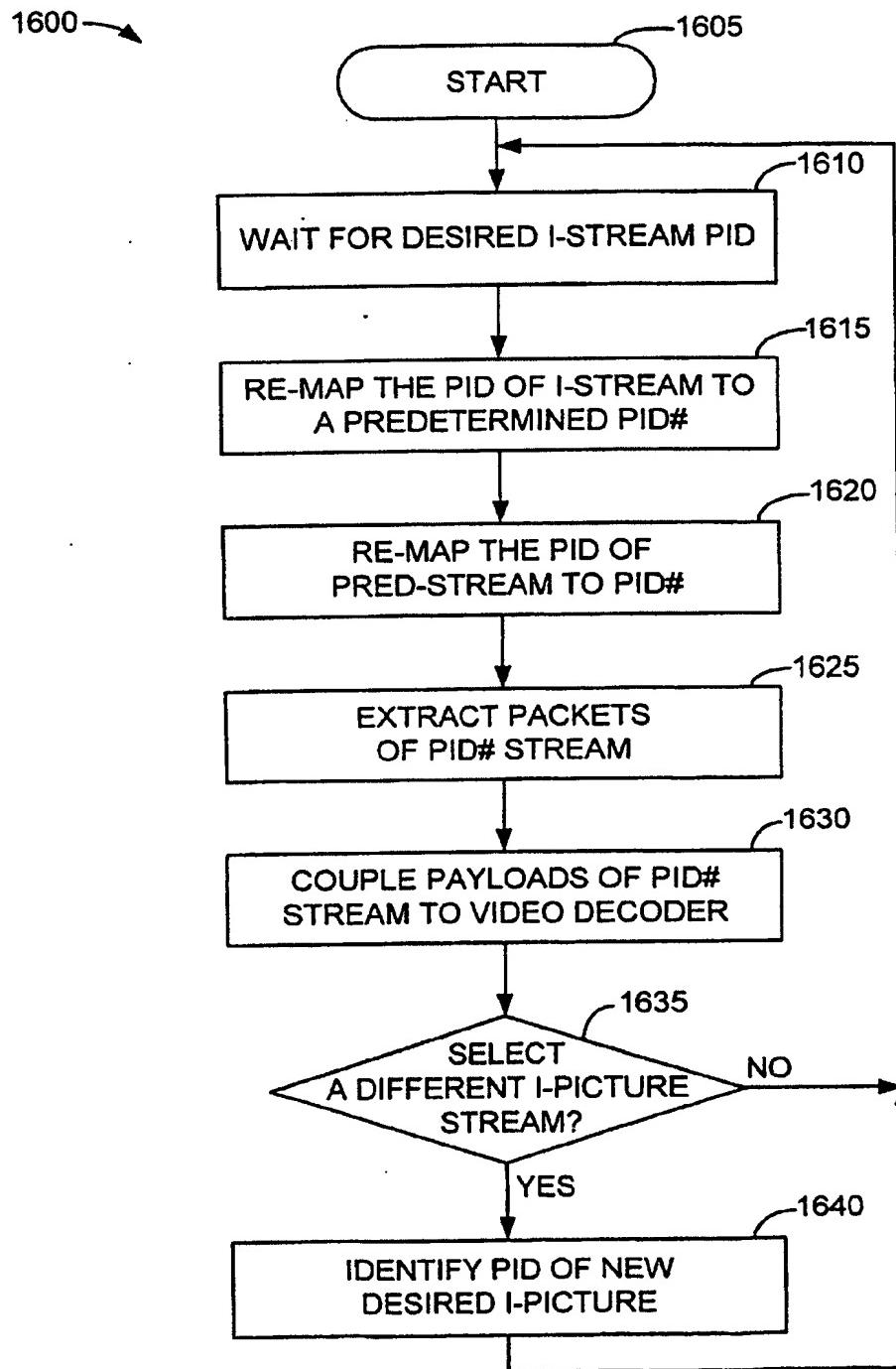


FIG. 16.



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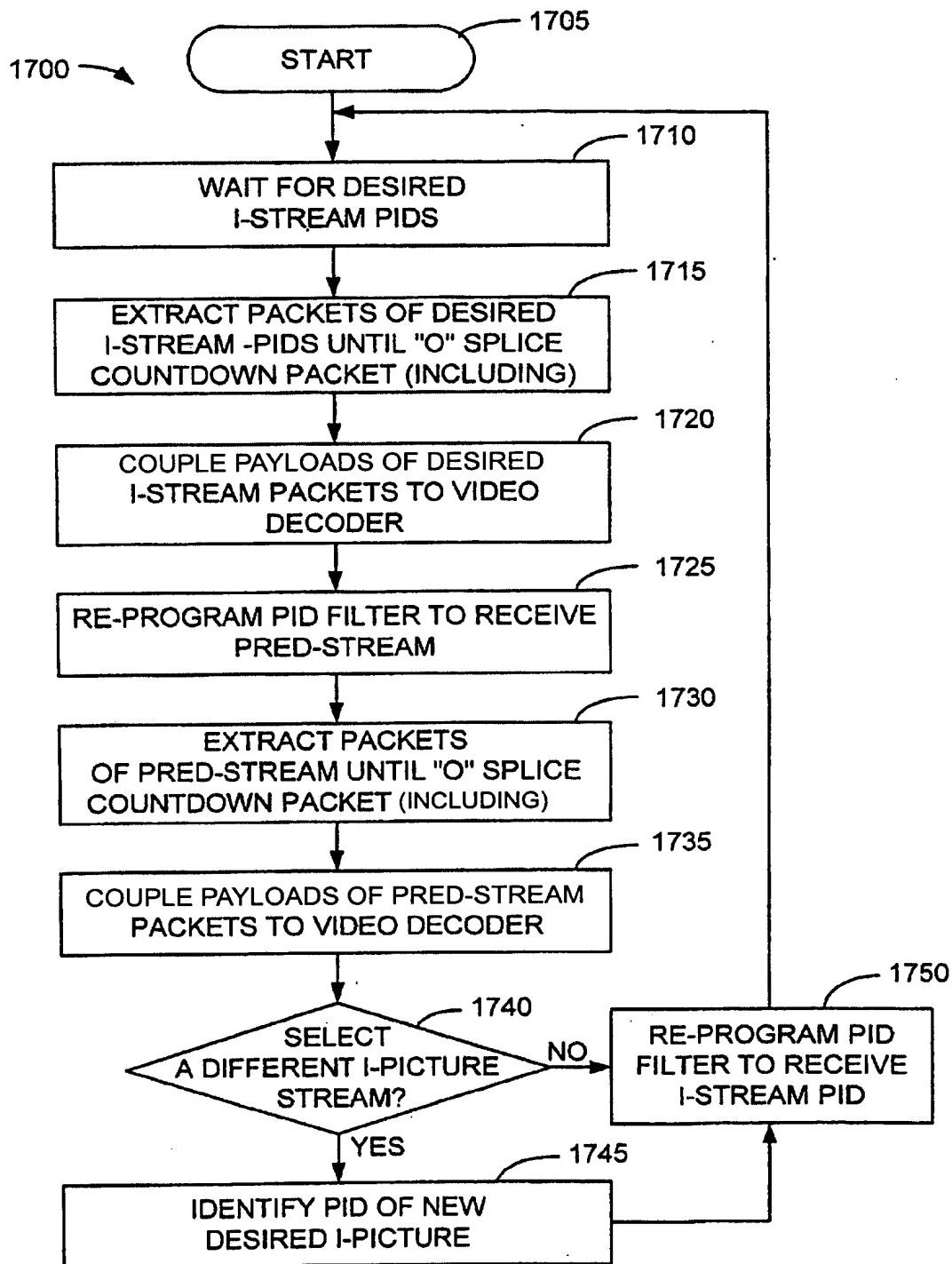


FIG. 17.

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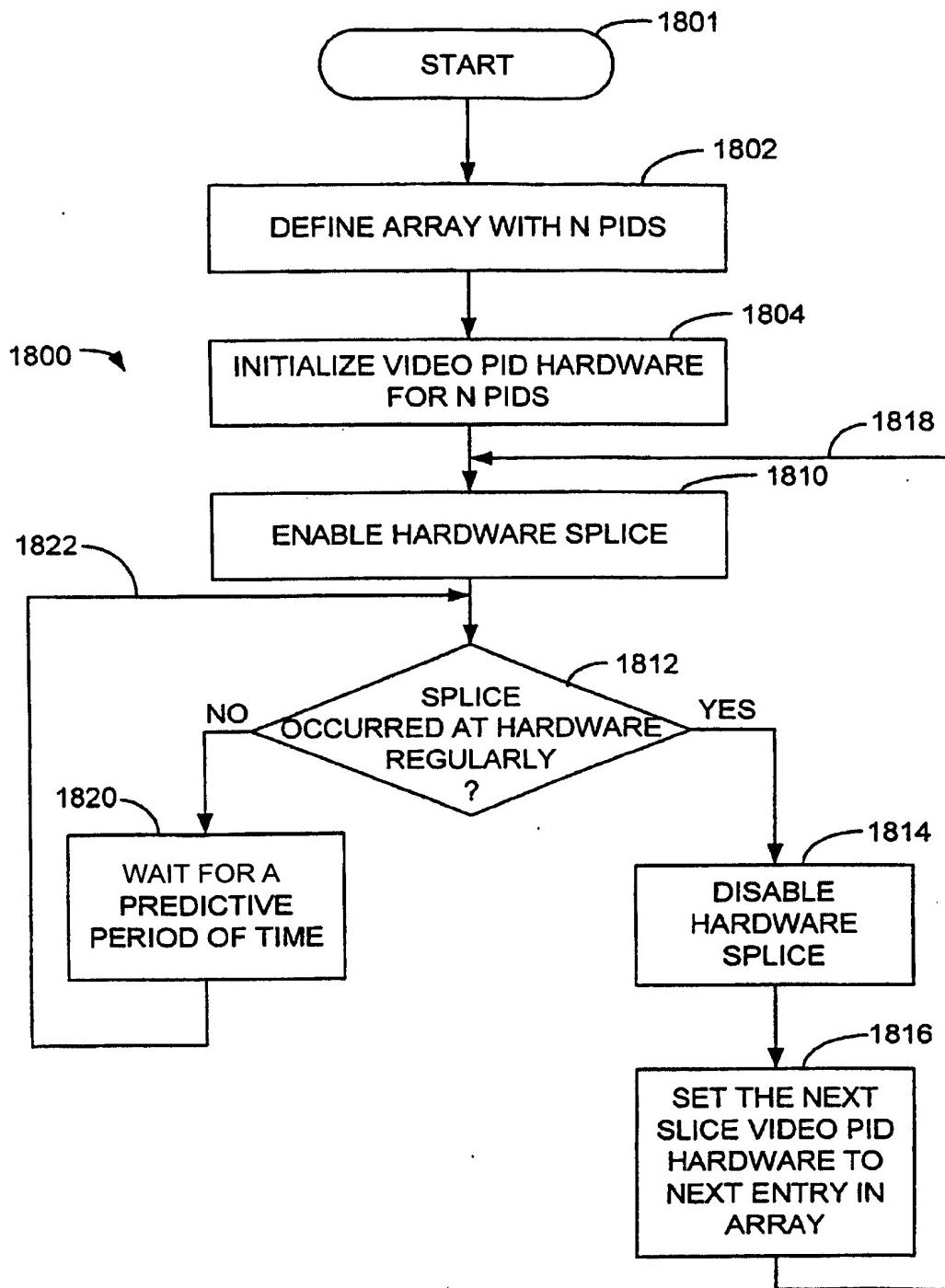


FIG. 18.



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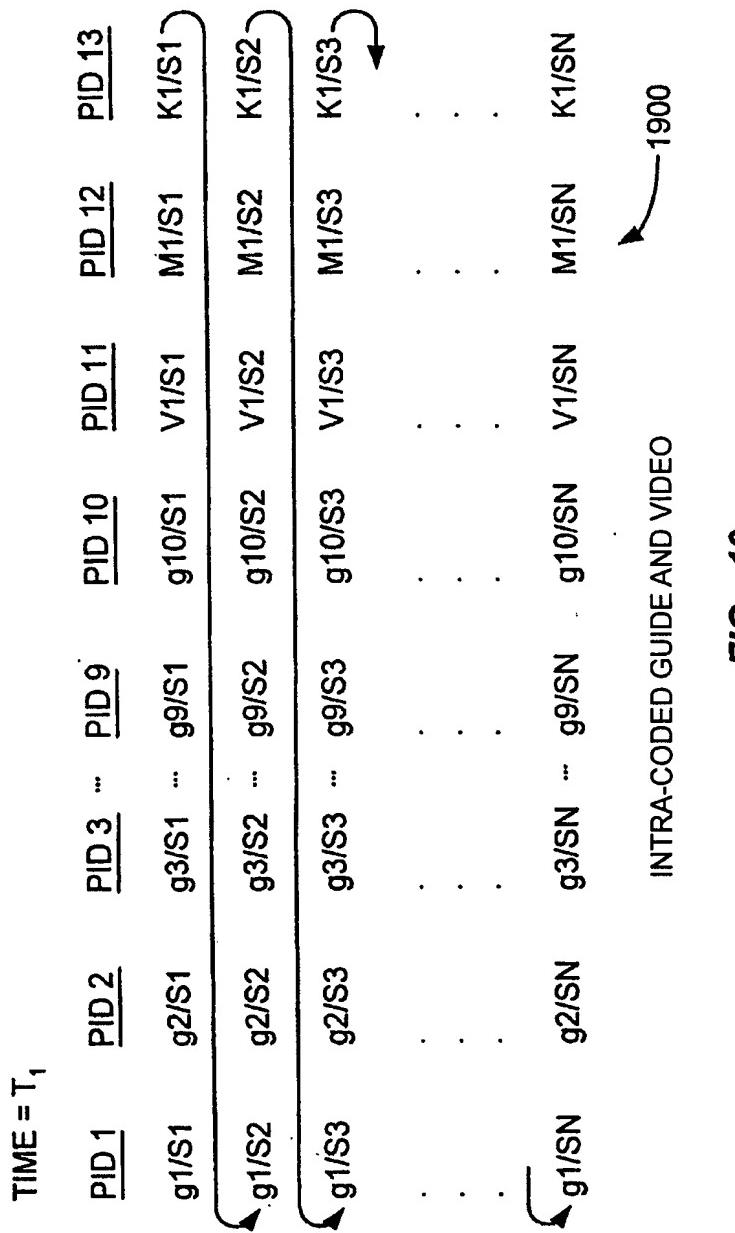


FIG. 19.



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<u>TIME</u>	<u>PID 11</u>	<u>PID 12</u>	<u>PID 13</u>	<u>PID 11</u>	<u>PID 12</u>	<u>PID 13</u>	<u>PID 11</u>	<u>PID 12</u>	<u>PID 13</u>
t_2	V2/S1	M2/S1	K2/S1	V2/S2	M2/S2	K2/S2	V2/SN	M2/SN	K2/SN
t_3	V3/S1	M3/S1	K3/S1	V3/S2	M3/S2	K3/S2	V3/SN	M3/SN	K3/SN
t_4	V4/S1	M4/S1	K4/S1	V4/S2	M4/S2	K4/S2	V4/SN	M4/SN	K4/SN
\dots	\dots	\dots	\dots	\dots	\dots	\dots	\dots	\dots	\dots
t_{15}	V15/S1	M15/S1	K15/S1	V15/S2	M15/S2	K15/S2	V15/SN	M15/SN	K15/SN

PREDICTED VIDEO

2000

2002

2003

2004

2015

FIG. 20.



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<u>TIME</u>	<u>PID 11</u>	<u>PID 12</u>	<u>PID 13</u>	<u>PID 11</u>	<u>PID 12</u>	<u>PID 13</u>	<u>PID 11</u>	<u>PID 12</u>	<u>PID 13</u>
t_2	SK/S1	SK/S1	SK/S1	SK/S2	SK/S2	SK/S2	SK/SN	SK/SN	SK/SN
t_3	SK/S1	SK/S1	SK/S1	SK/S2	SK/S2	SK/S2	SK/SN	SK/SN	SK/SN
t_4	SK/S1	SK/S1	SK/S1	SK/S2	SK/S2	SK/S2	SK/SN	SK/SN	SK/SN
\dots	\dots	\dots	\dots	\dots	\dots	\dots	\dots	\dots	\dots
t_{15}	SK/S1	SK/S1	SK/S1	SK/S2	SK/S2	SK/S2	SK/SN	SK/SN	SK/SN

SKIPPED GUIDE

2100

FIG. 21.

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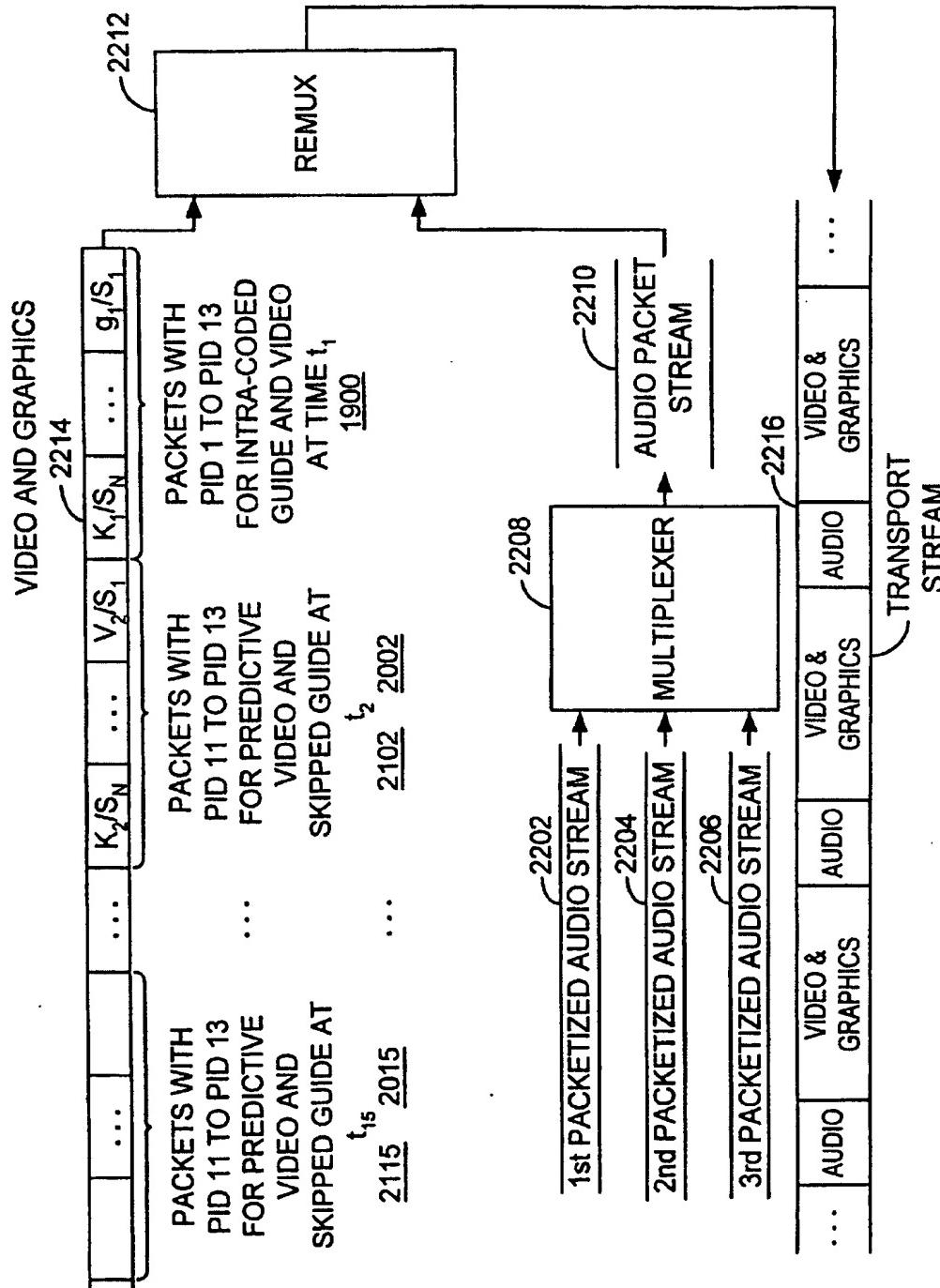


FIG. 22.



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O_1/S_1	O_2/S_1	O_3/S_1
\vdots	\vdots	\vdots
O_1/S_N	O_2/S_N	O_3/S_N
O_1/S_{N+1}	O_2/S_{N+1}	O_3/S_{N+1}
\vdots	\vdots	\vdots
O_4/S_{2N}	O_5/S_{2N}	O_6/S_{2N}
O_7/S_{2N+1}	O_8/S_{2N+1}	O_9/S_{2N+1}
\vdots	\vdots	\vdots
O_7/S_{3N}	O_8/S_{3N}	O_9/S_{3N}

0_1	0_2	0_3
0_4	0_5	0_6
0_7	0_8	0_9

OBJECTS

(A)

SLICE-BASED PARTITIONING

(B)

FIG. 23.

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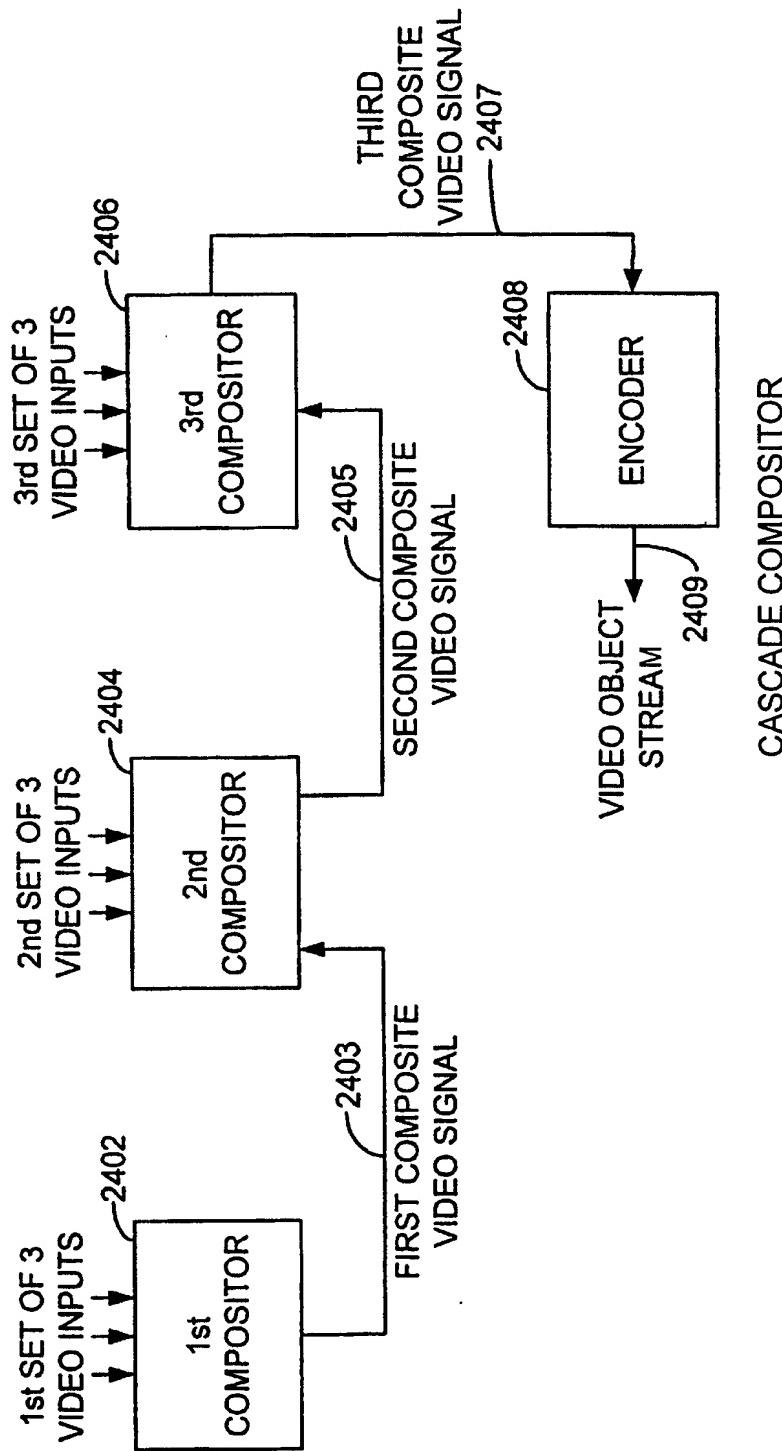


FIG. 24.



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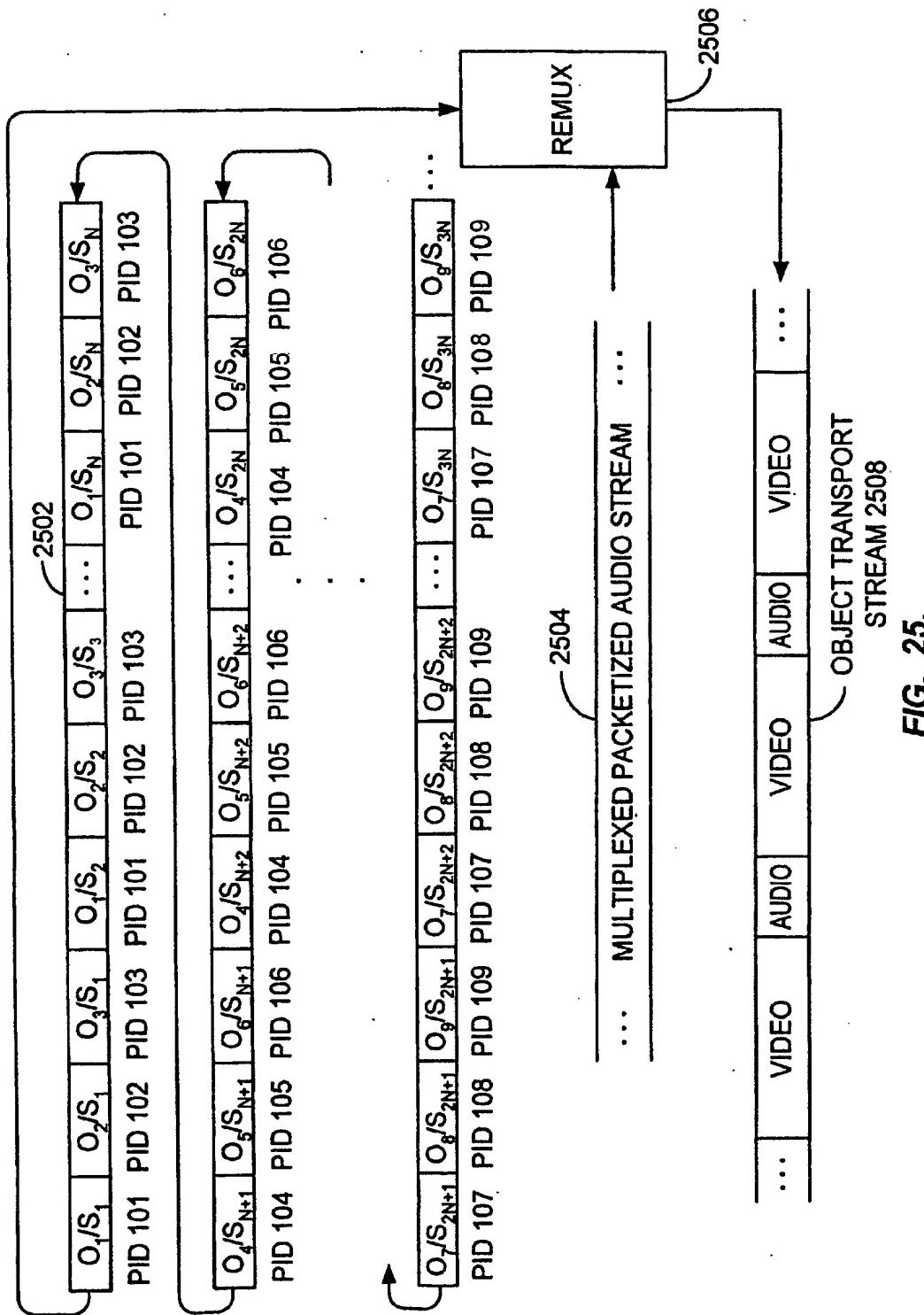


FIG. 25.



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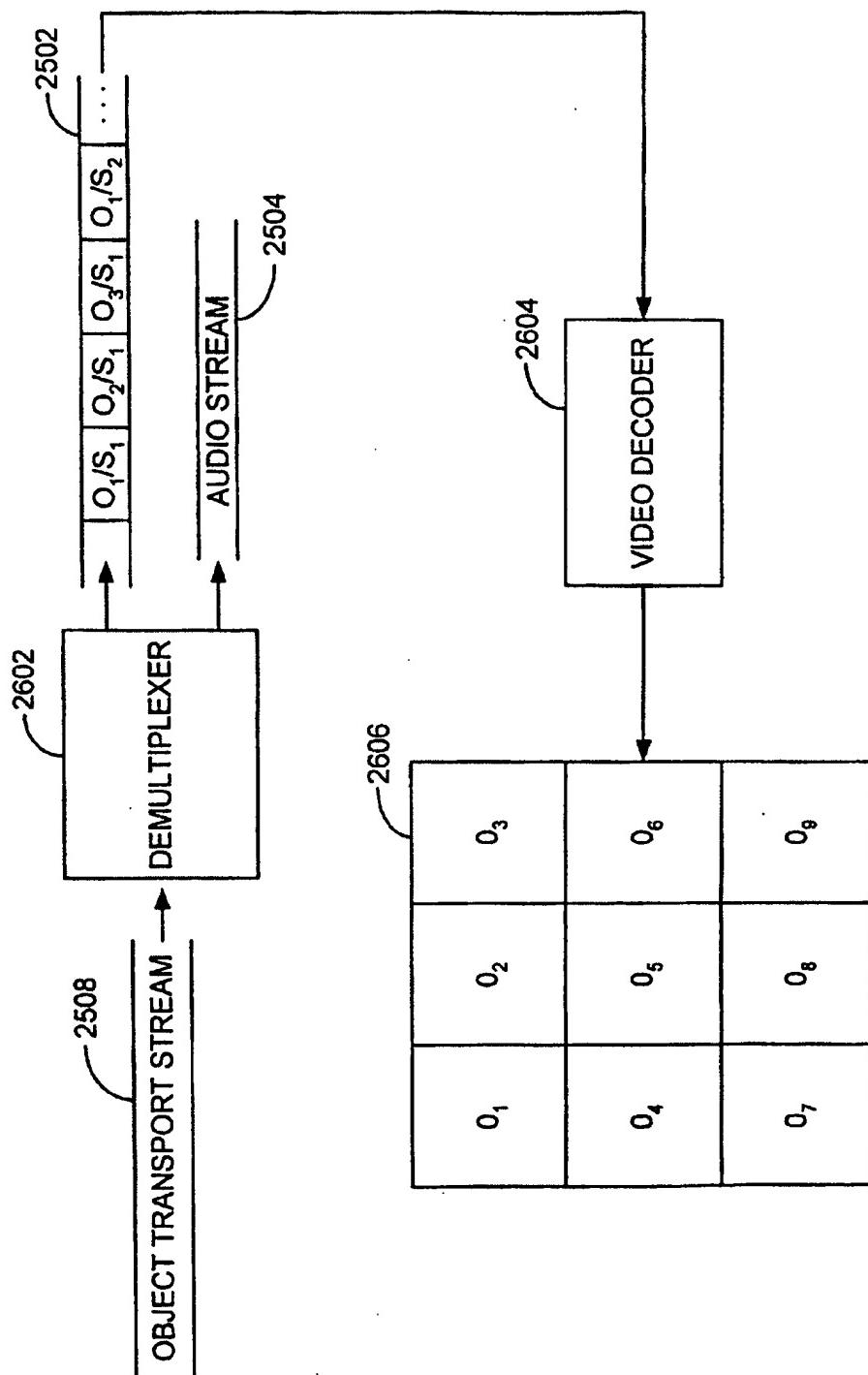


FIG. 26.

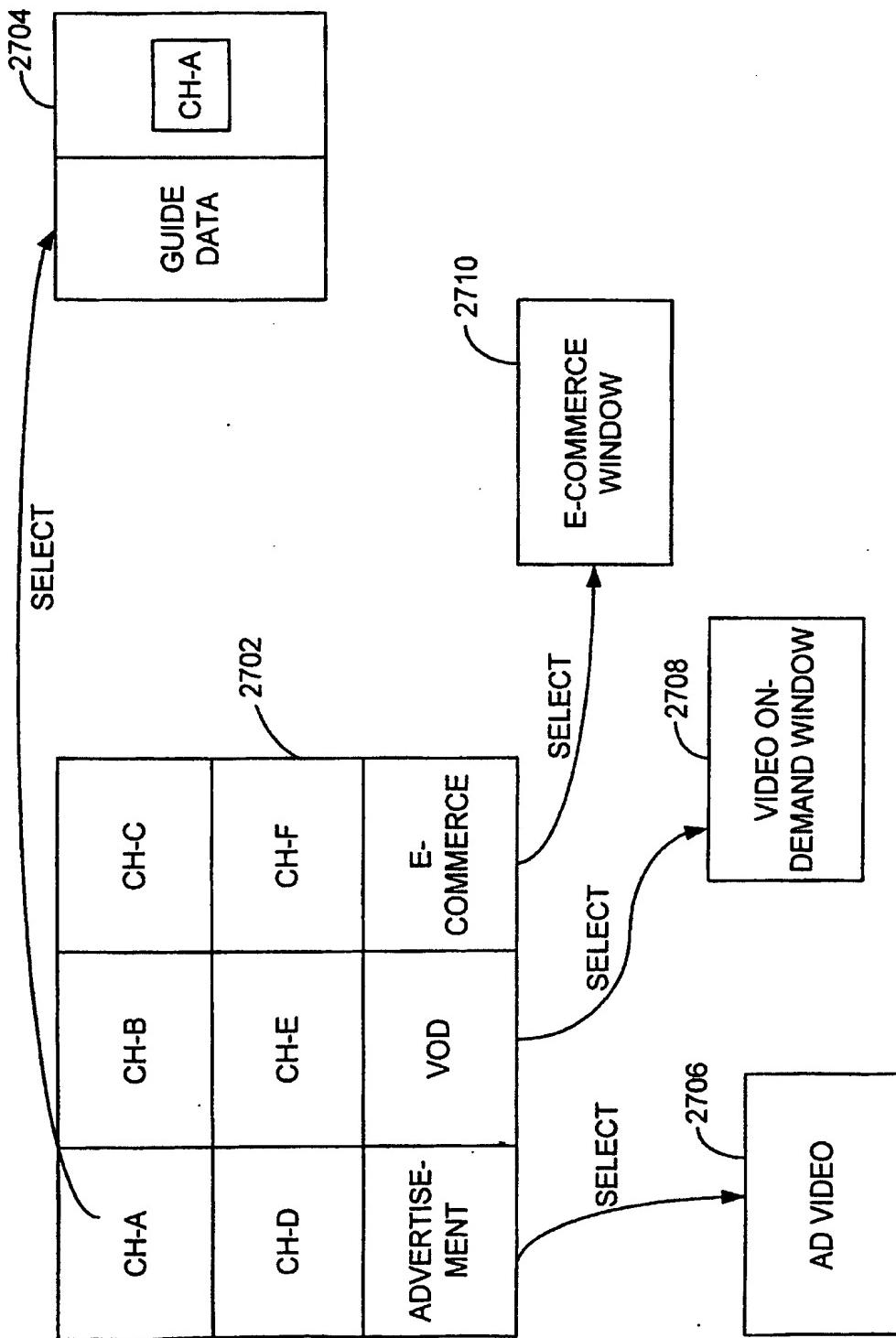


FIG. 27.



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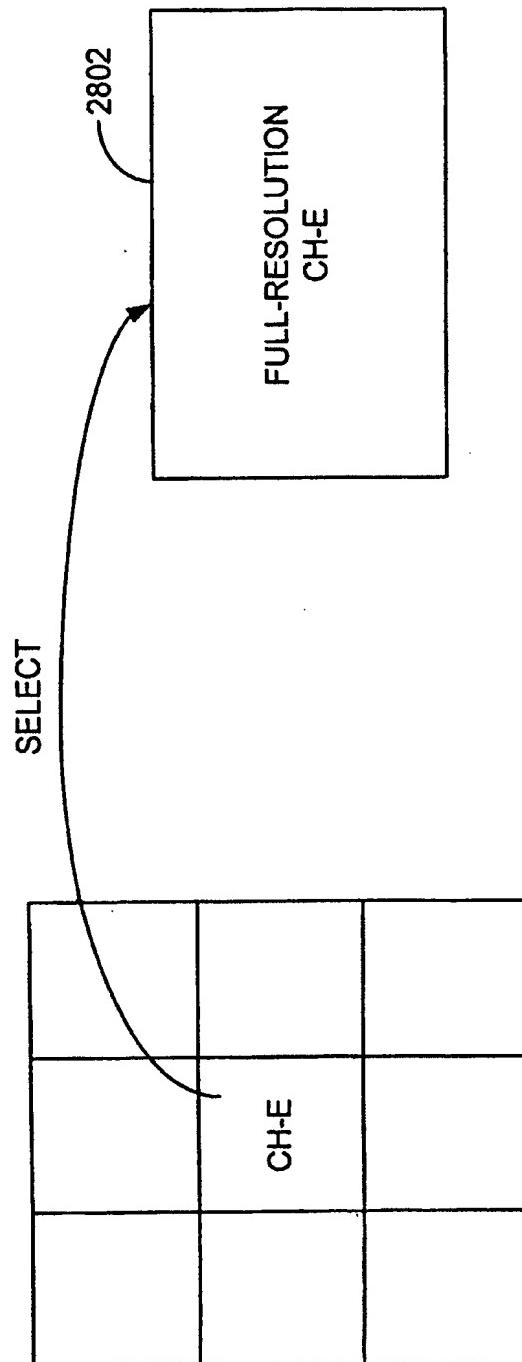


FIG. 28.



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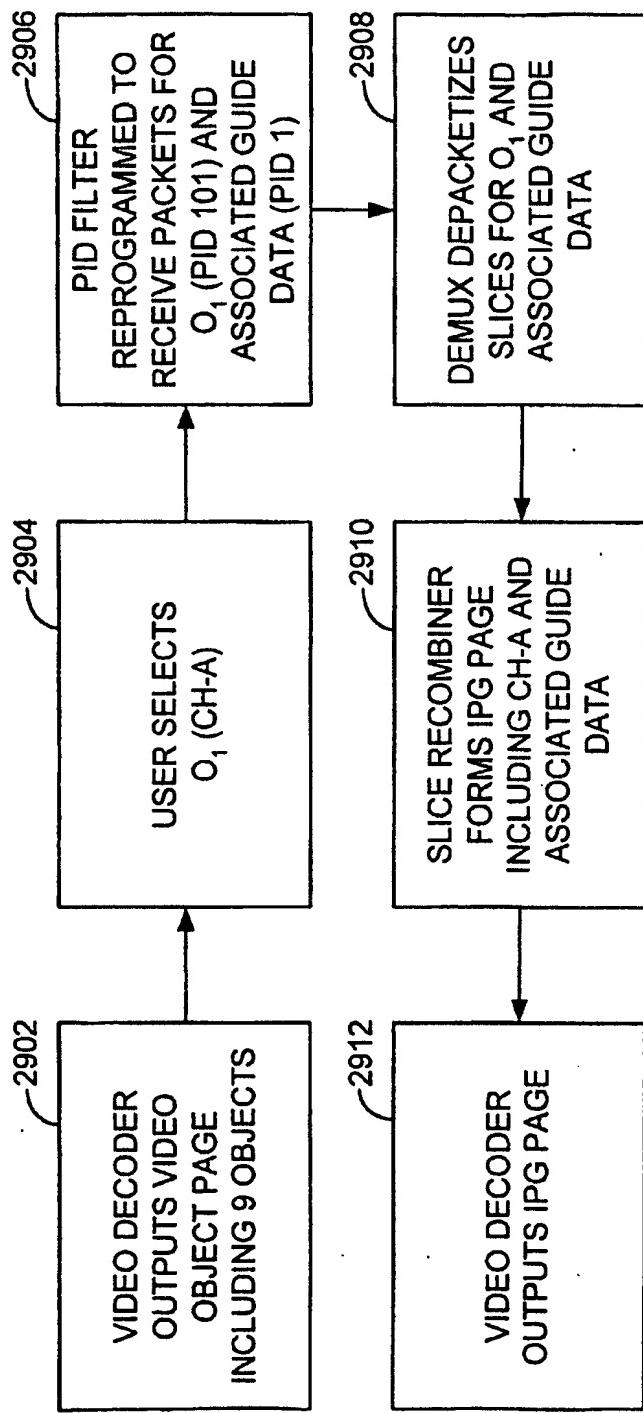


FIG. 29.



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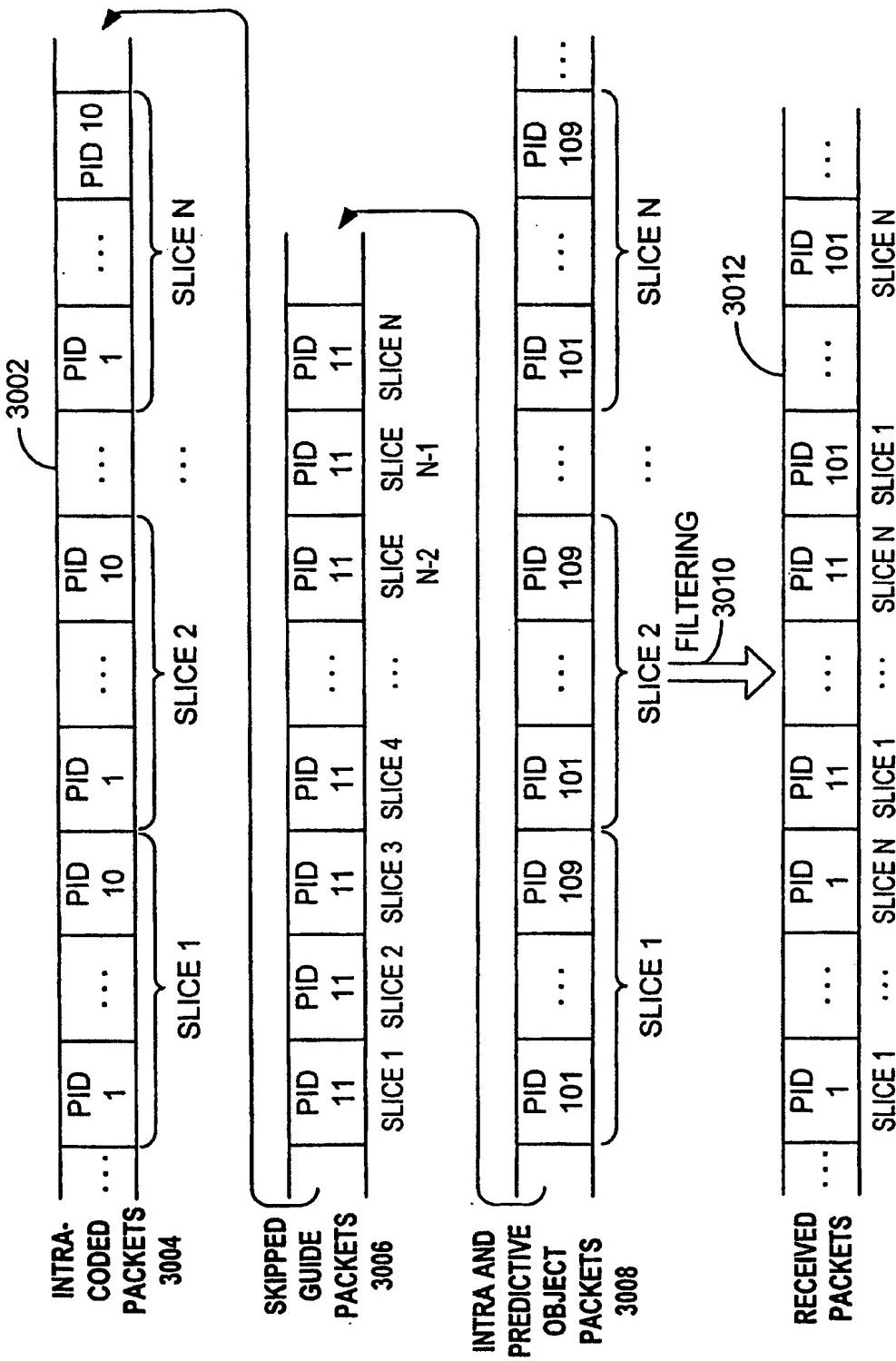


FIG. 30.



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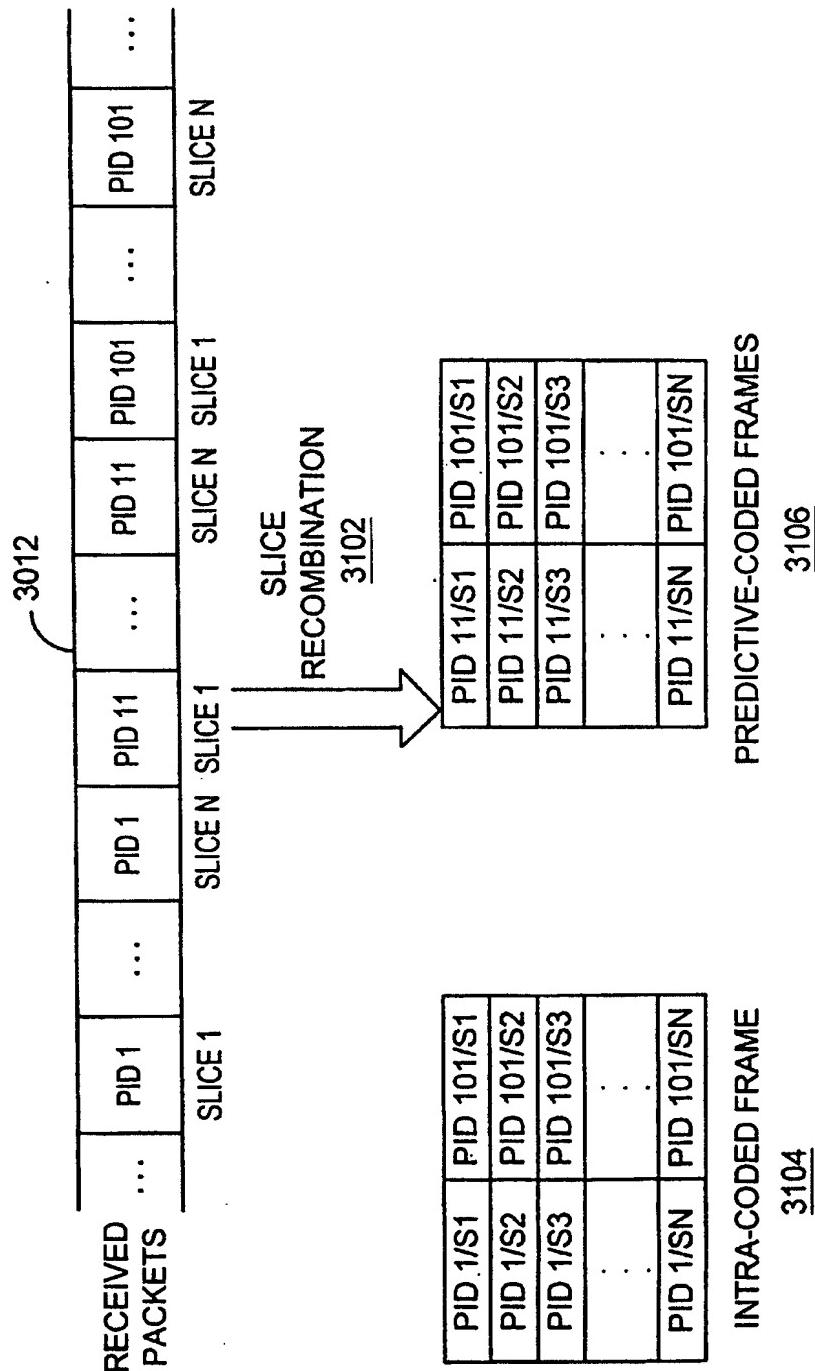


FIG. 31.

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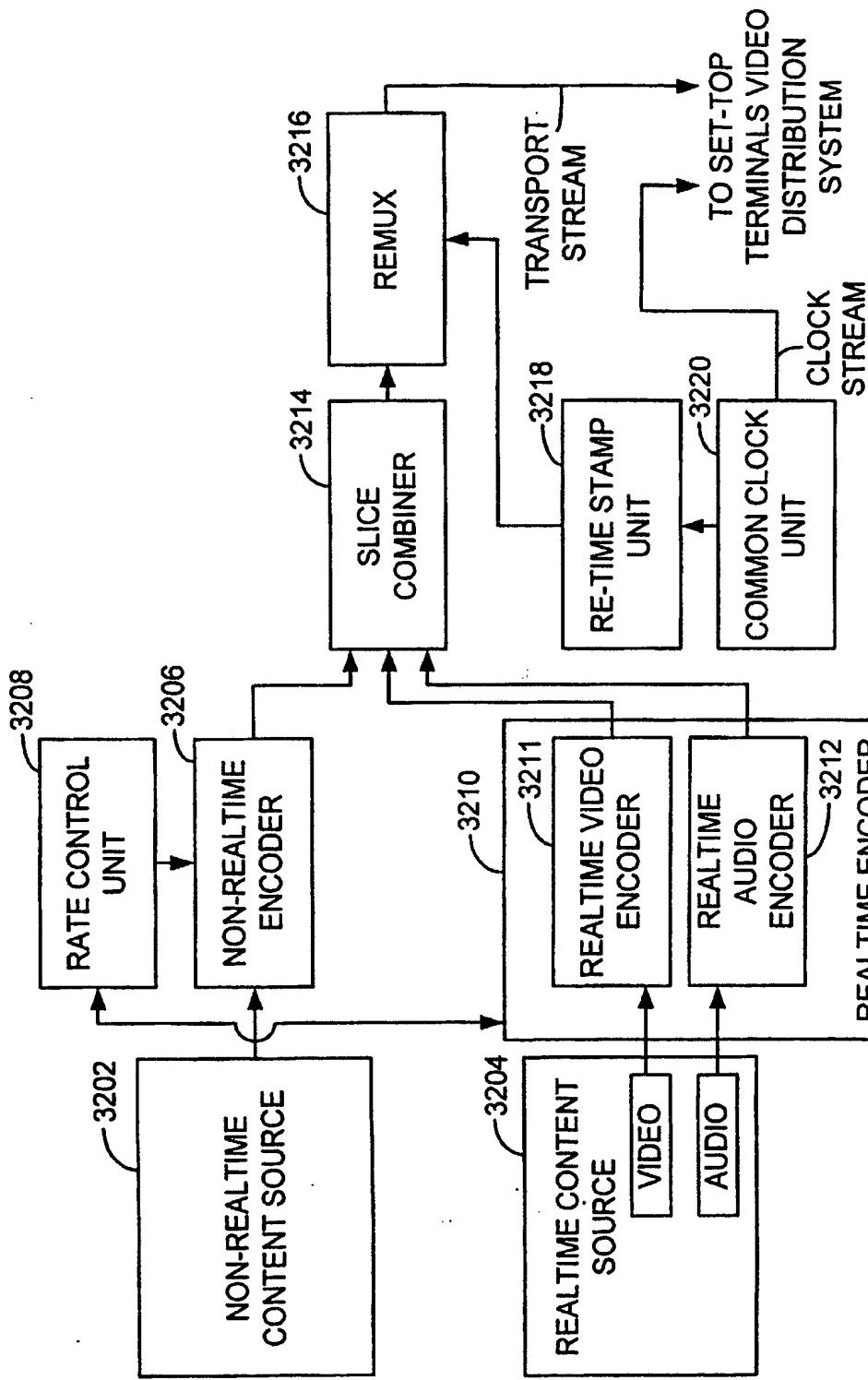


FIG. 32.
RE-TIMESTAMPING AND RATE CONTROL APPARATUS